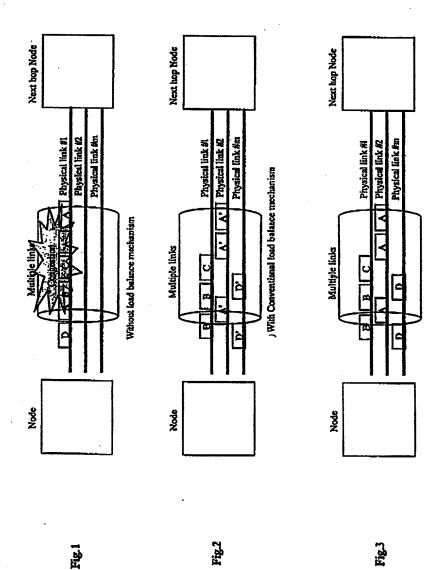
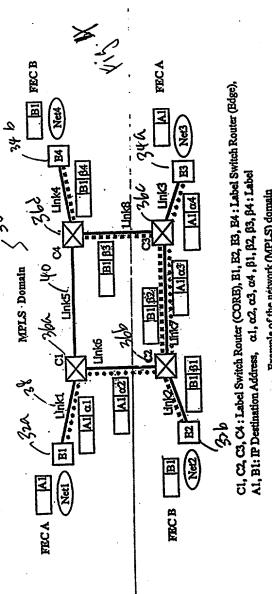
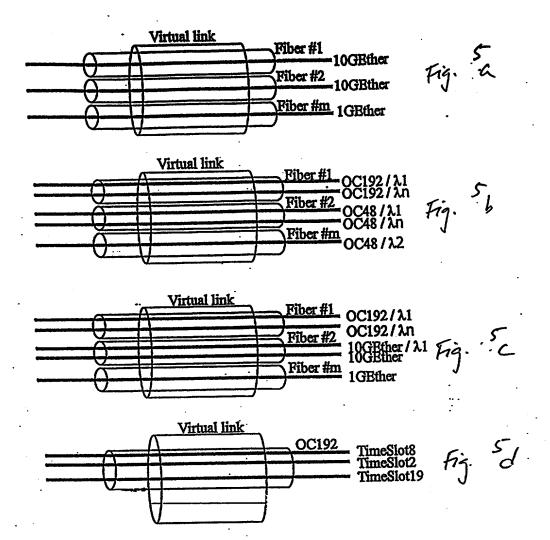
Fig.1





Bxample of the network (MPLS) domain



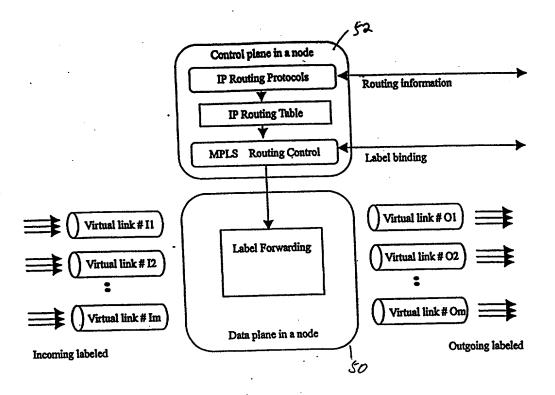
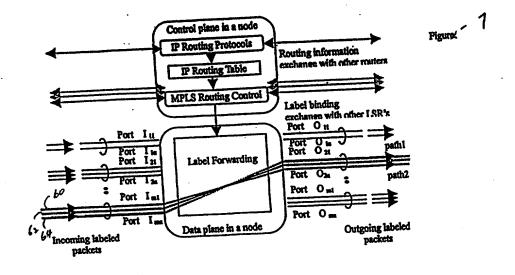
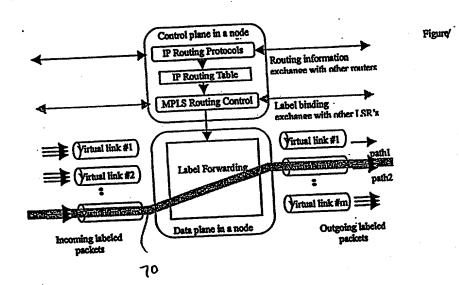
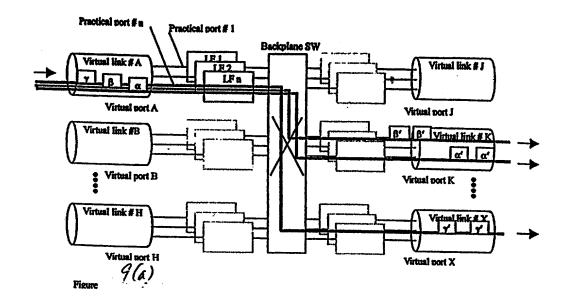
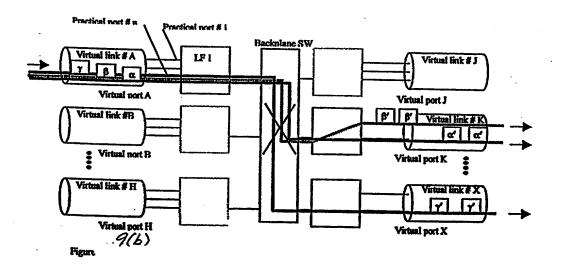


Figure . LSR Architecture









input practical port # Output practical port # Output virtual port# n $f(\alpha')$ f(β') n $f(\gamma')$ 2 f(δ') 1 f(ε') 2 f(ζ') 1 f(η') 5 f(o') f(k')

Figure : Label Forwarding Table and mechanism to decide practical output port at the sending LSR

	98	100	Q O	92 maintained by the M	94 PLS control plane	.48
Tag	Hash value of incoming label	Input practical port #	Input Input Virtual portur	it Ontoid	Output	Output practical port #
0	f(a')	n	1 A 3 10		K	f(a")
0	f(B')	ņ	X X B	Bin	K	f(B")
0	f(Y)	n	A		1 x	f(Y")
•	f(8')	2	Α		A Box	f(8")
0	f(e')	1	L. A. C. C.			f(e")
0	f(C')	2	A		W. W.	f(C")
0	f(n')	1	A			f(n")
	f(d')	5*	A A G	1 1305	K	f(o")
	f(K')	n ·	A		, x	f(k")

Figure Fast lookup mechanism using a hash